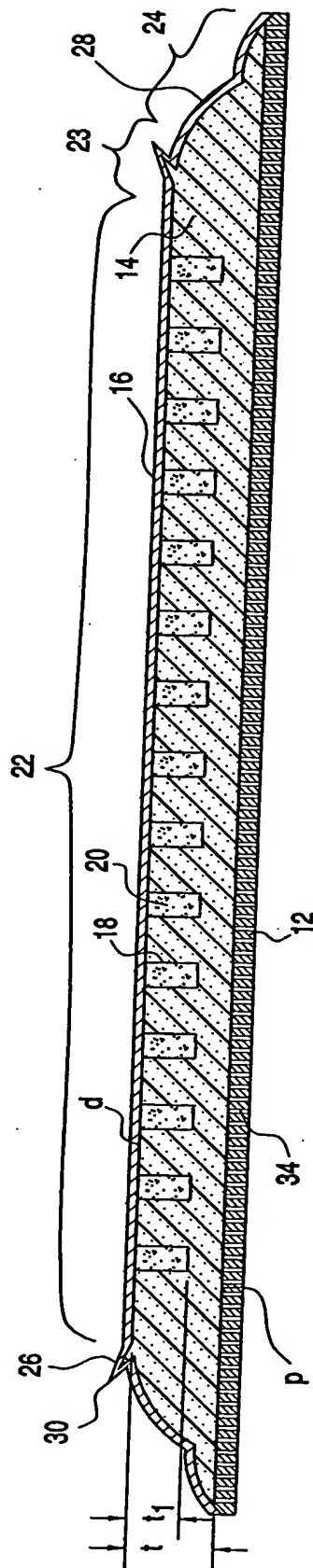
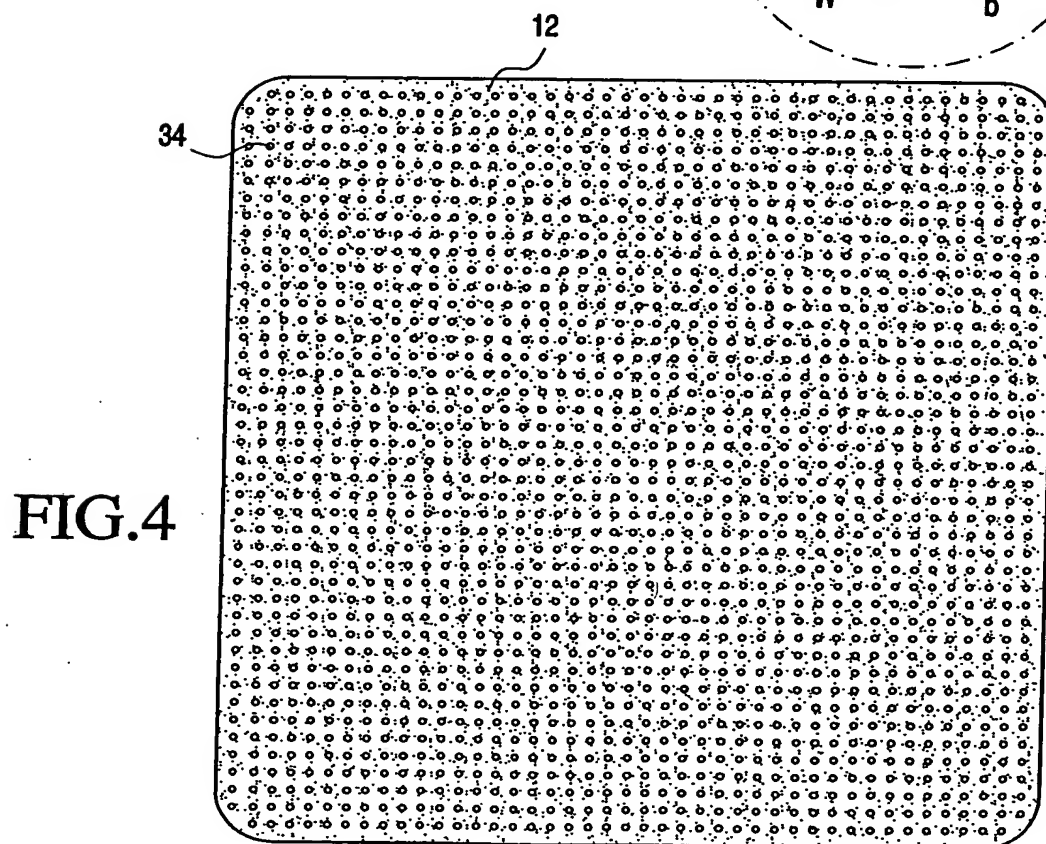
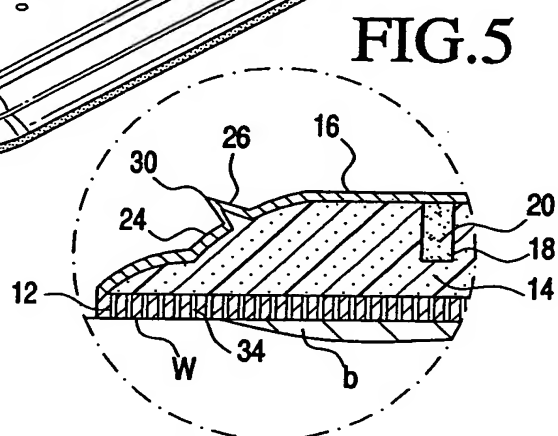
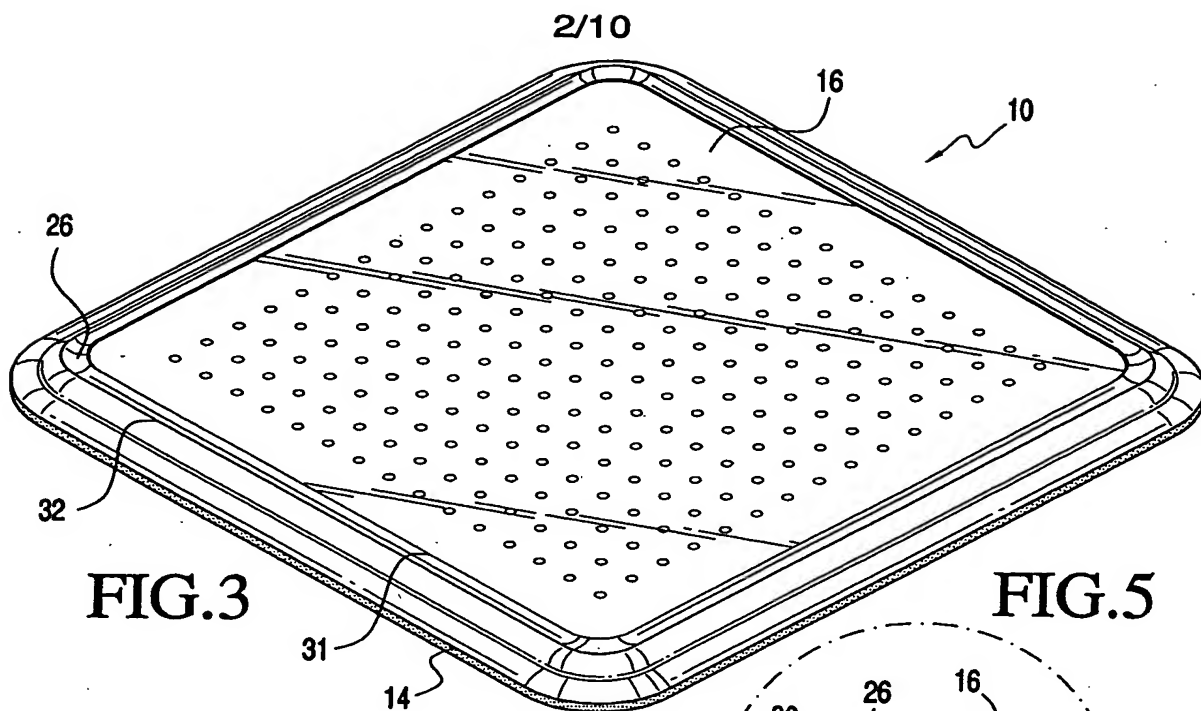


FIG. 1

FIG. 2





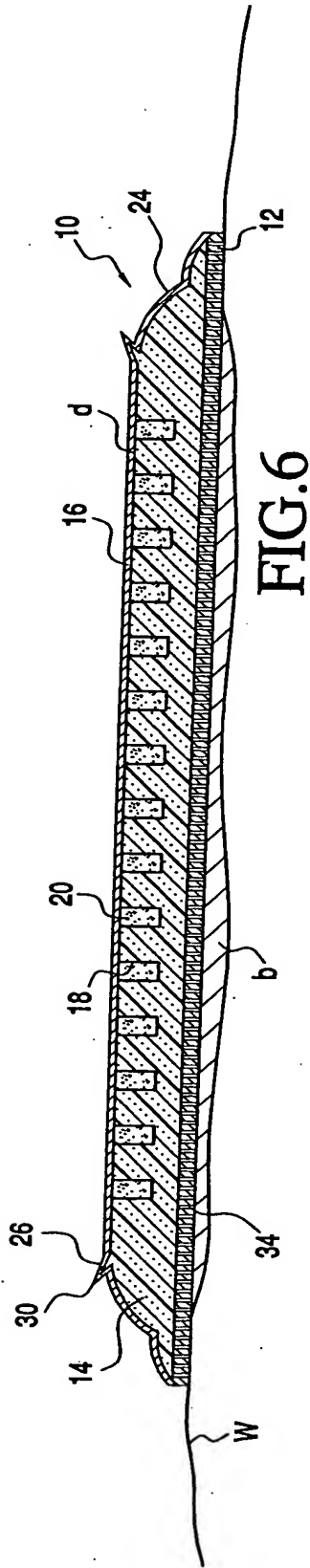
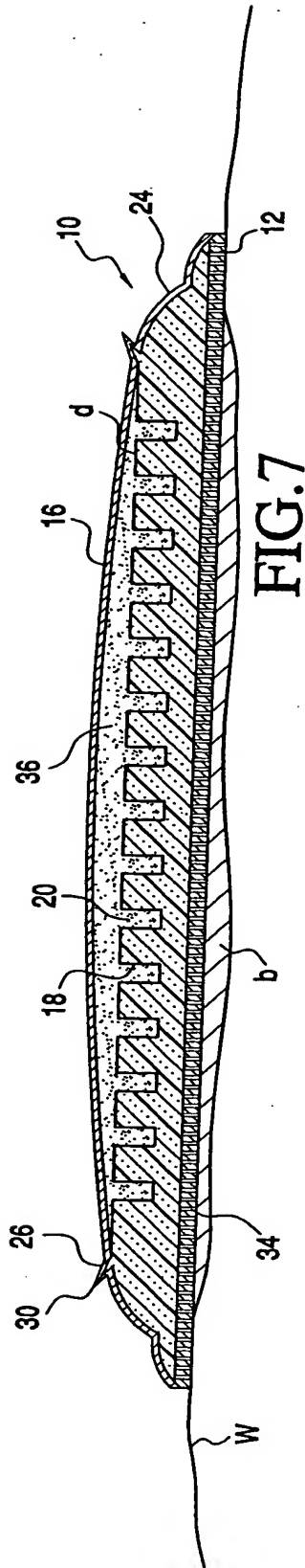


FIG. 6



**FIG. 7**

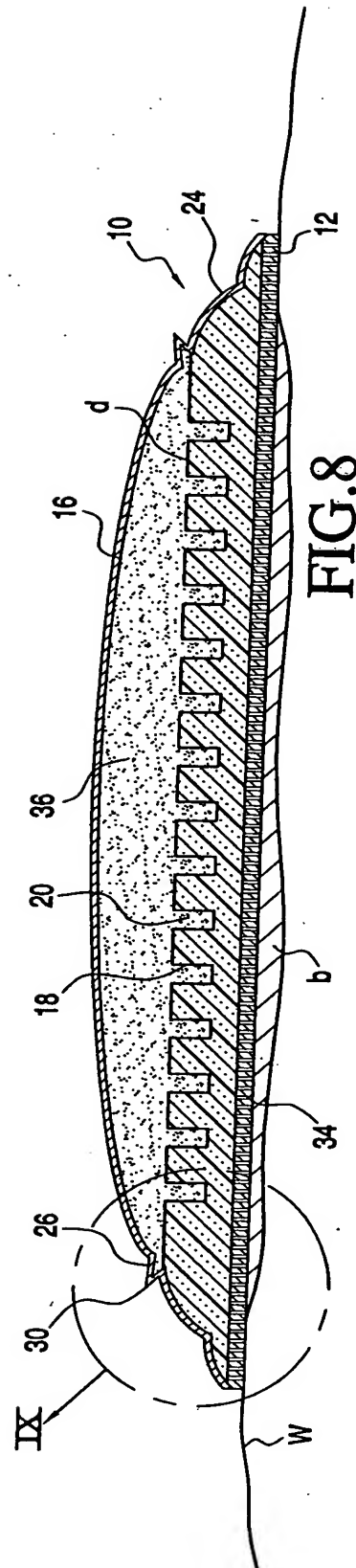


FIG. 8

Figure 1 is a schematic diagram of a square-shaped substrate 12 with rounded corners. The substrate has a central region 34 defined by a dense grid of small dots. The outer region of the substrate is labeled 19.

FIG. 1 is a schematic diagram of a square-shaped device 14. The device features a rounded outer frame 18 and a central array of small circular elements 22. Two diagonal lines 24 intersect at the center of the array.

FIG.13

FIG.14

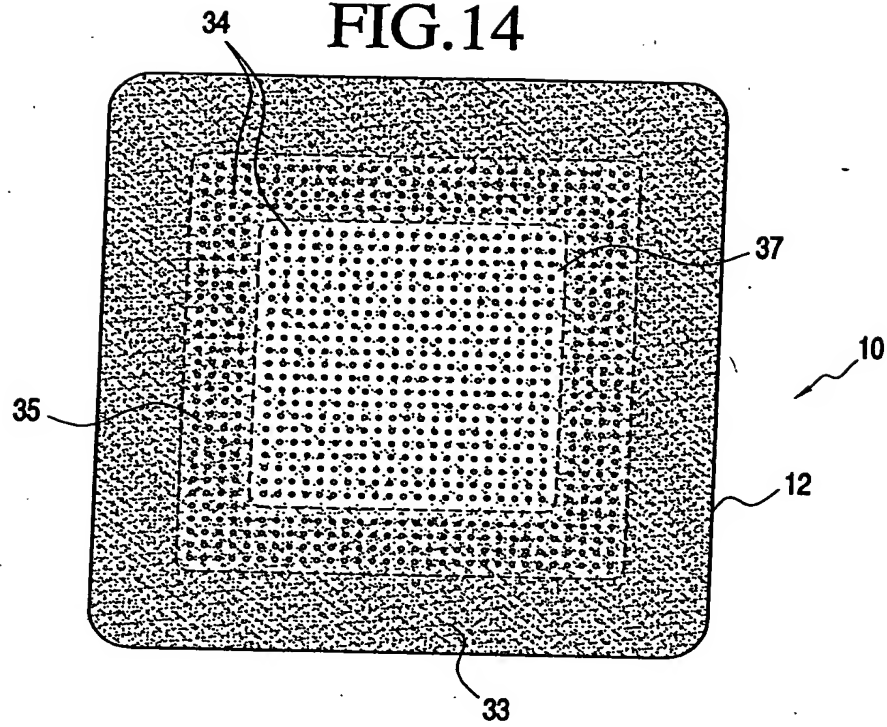


FIG.15

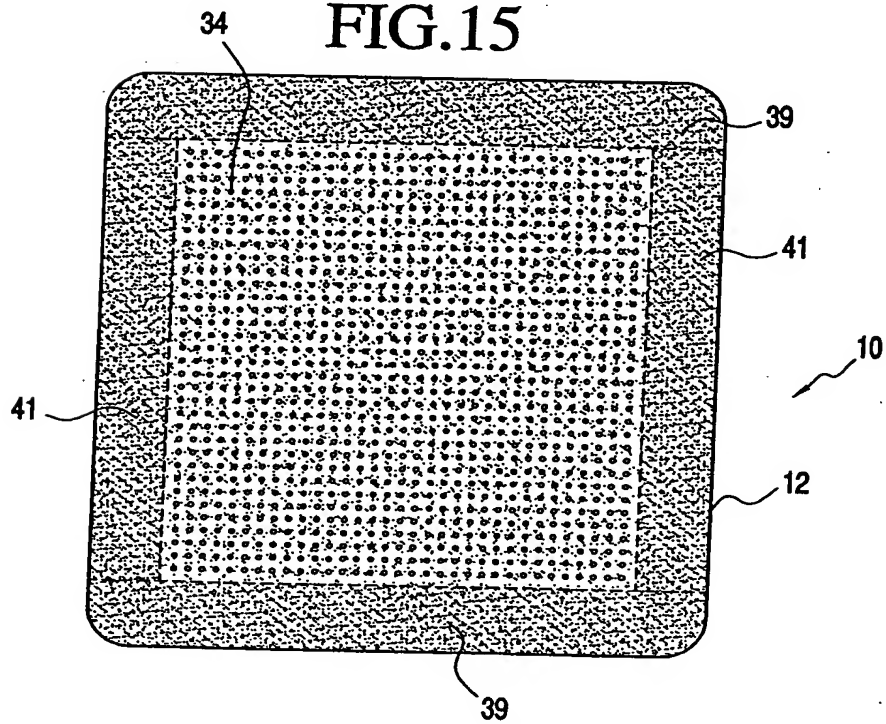


FIG.16

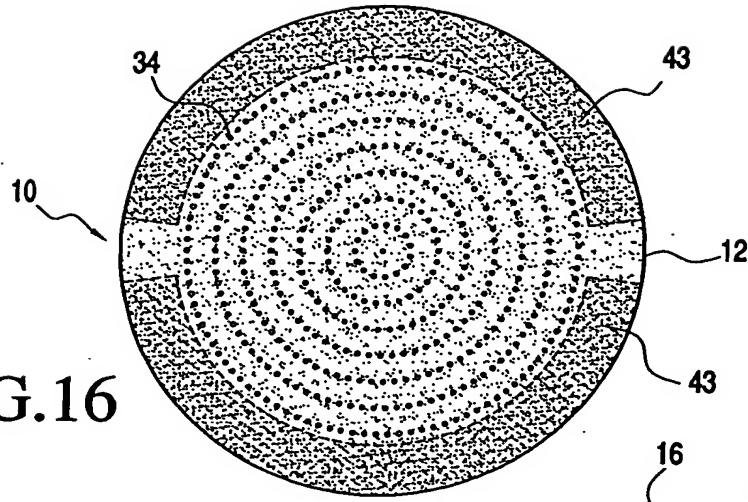


FIG.17

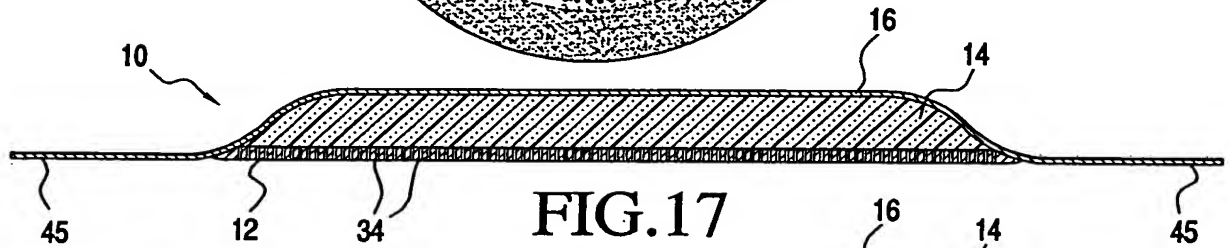


FIG.18



FIG.19

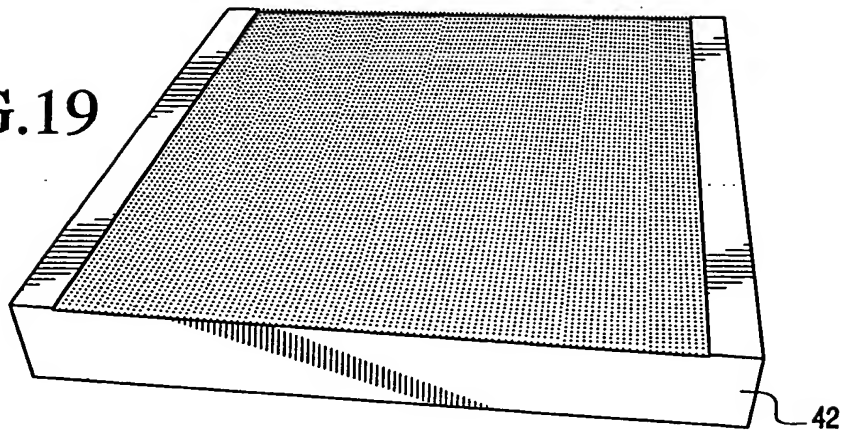
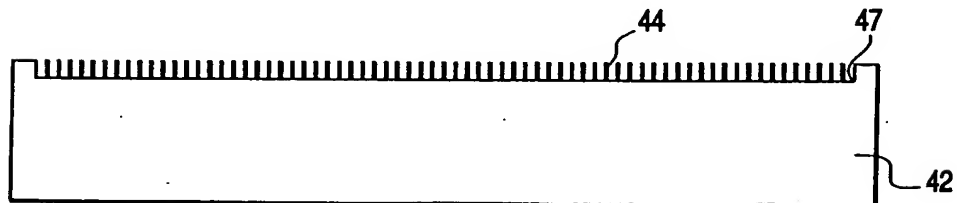


FIG.20



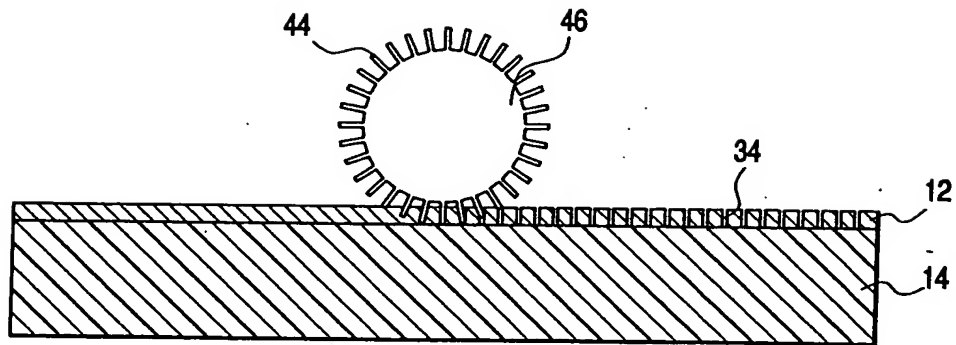


FIG. 21

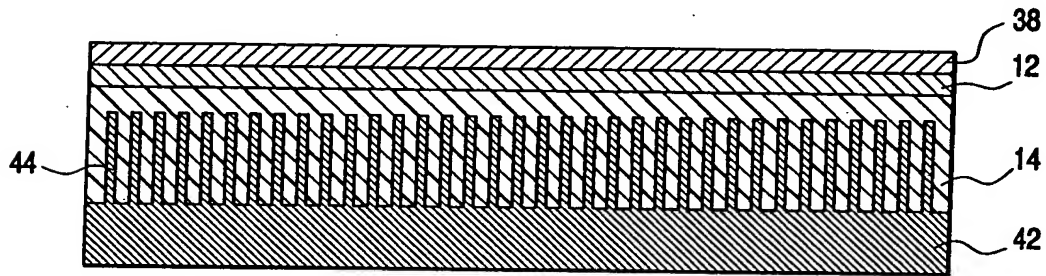


FIG. 22

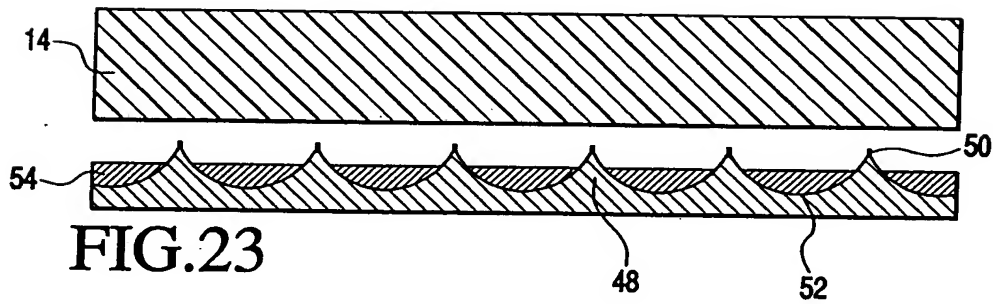


FIG. 23

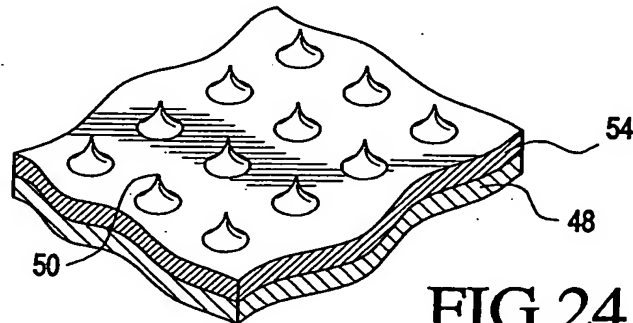
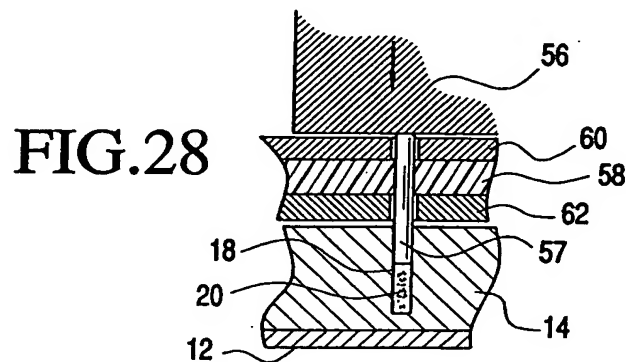
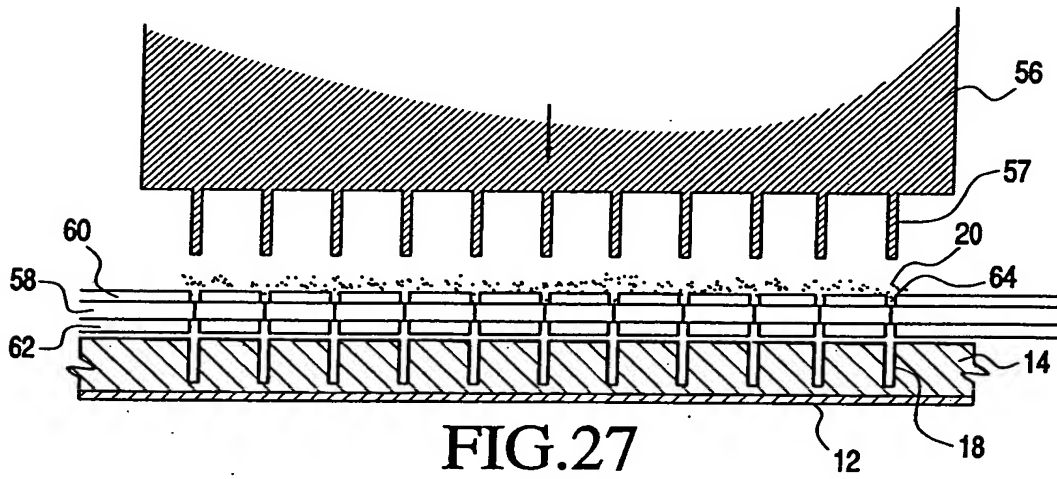
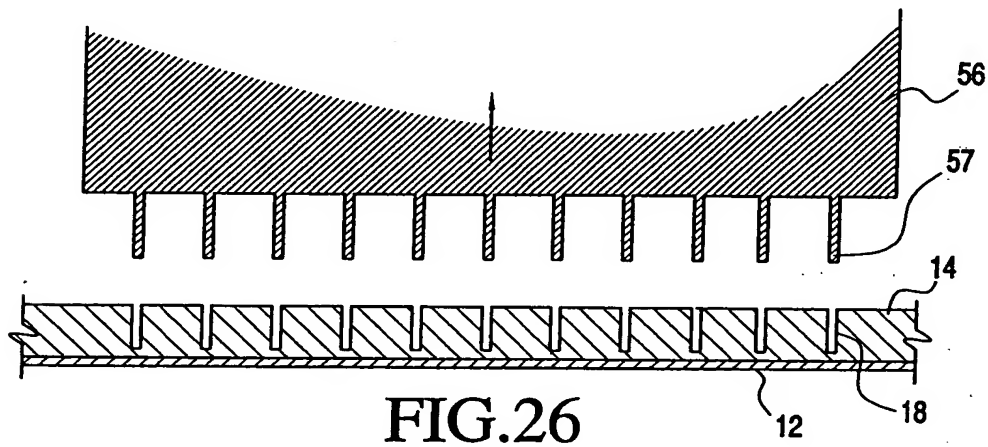
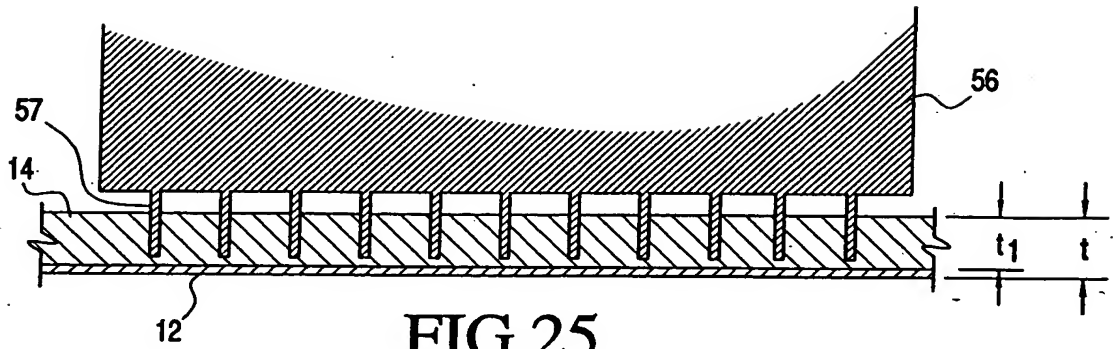


FIG. 24





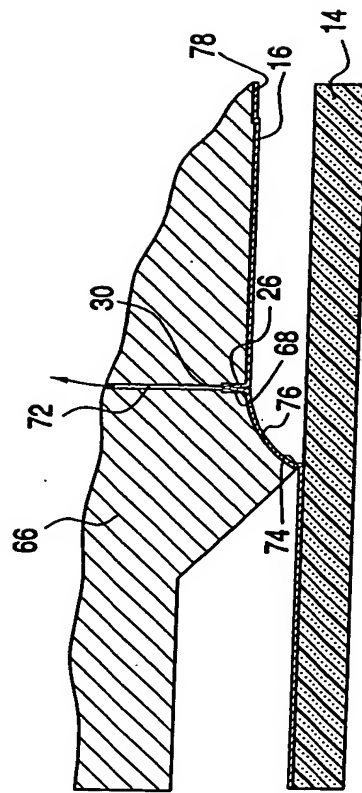
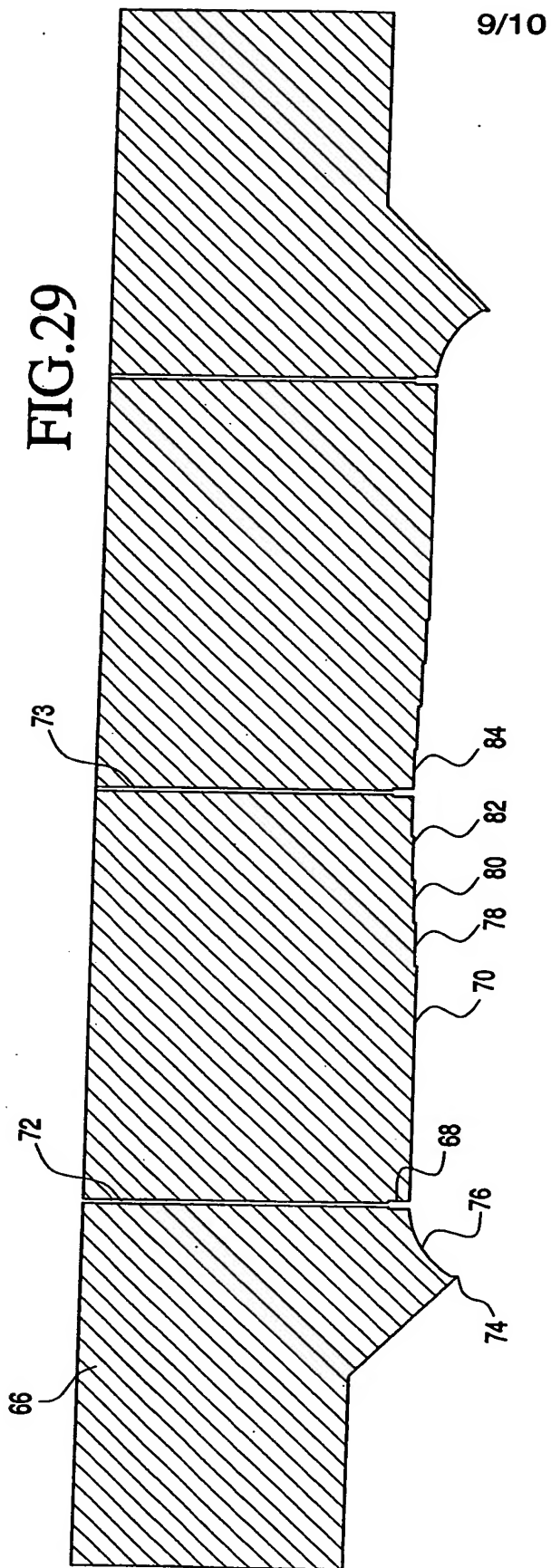


FIG. 31

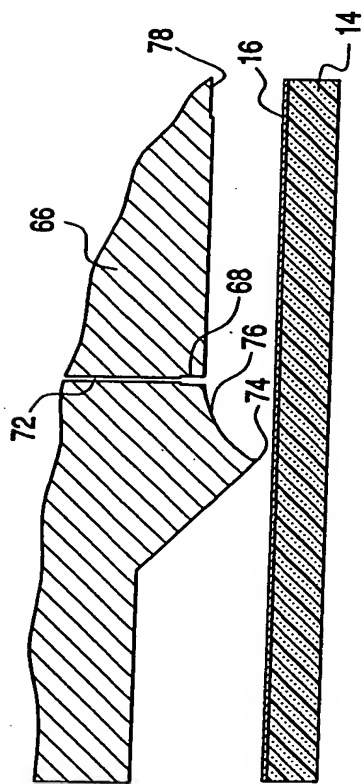


FIG. 30

